

**IN THE CLAIMS**

Please cancel claims 9, 10 and 35 thru 43 without prejudice or disclaimer, and amend claims 1, 2, 4, 5, 11, 17, 18, 23, 24 and 28, as follows:

1           1. (Currently Amended)   A flat panel display apparatus for receiving display  
2 information including video data and synchronizing data from a host, said flat panel display  
3 apparatus comprising:

4           a receiver for receiving and reconstructing said display information;

5           a synchronizing signal generator having an input connected to an output of said receiver  
6 for generating a synchronizing signal by extracting the synchronizing data from said  
7 reconstructed display information;

8           a digital-to-analog converter (DAC) for converting said video data from said  
9 reconstructed display information to a corresponding analog video signal; and

10          an output terminal connected to said synchronizing signal generator and to said DAC for  
11 ~~externally~~ transferring said synchronizing signal and said corresponding analog video signal to  
12 an analog display apparatus.

1           2. (Currently Amended)   A flat panel display apparatus for receiving display  
2 information including video data and synchronizing data from a host, said flat panel display  
3 apparatus comprising:

4           a receiver for receiving and reconstructing said display information;

5           a synchronizing signal generator having an input connected to an output of said receiver

6 for generating a synchronizing signal by extracting the synchronizing data from said  
7 reconstructed display information;

8 a video data converter connected to the output of said receiver for converting said video  
9 data so as to correspond to a prescribed display mode;

10 a digital-to-analog converter (DAC) connected to an output of said video data converter  
11 for converting said converted video data from said video data converter to a corresponding  
12 analog video signal; and

13 an output terminal connected to an output of said synchronizing signal generator and to  
14 an output of said DAC for ~~externally~~ transferring said synchronizing signal and said  
15 corresponding analog video signal to an analog display apparatus.

Claim 3. (Canceled)

1 4. (Currently Amended) The display apparatus of claim 1, further comprising:  
2 a video data converter connected between an output of said receiver and an input of said  
3 DAC for converting said video data;

4 a liquid crystal display (LCD) driver for receiving the converted video data ~~output~~ from  
5 said video data converter and producing a driver output; and

6 an LCD display panel for receiving ~~[[an]]~~ the driver output from said LCD driver.

1 5. (Currently Amended) The display apparatus of claim 1, said analog display  
2 apparatus comprising:

3 an amplifier for receiving said analog video signal from said DAC via said output  
4 terminal, and for amplifying said analog video signal;

5 a deflection signal generator for receiving said synchronizing signal ~~output~~ from said  
6 synchronizing signal generator via said output terminal, and for generating deflection signals;

7 a high voltage generator having an input connected to said deflection signal generator  
8 for receiving an output from said deflection signal generator, and for generating a high voltage;  
9 and

10 a cathode ray tube (CRT) display for receiving said amplified analog video signal from  
11 said amplifier, said deflection signals from said deflection signal generator, and ~~[[a]]~~ said high  
12 voltage from said high voltage generator.

Claims 6 thru 10. (Canceled)

1 11. (Currently Amended) The display apparatus of claim 1, further comprising a video  
2 data converter connected between the output of said receiver and an input of said DAC for  
3 converting said video data so as to correspond to a prescribed display mode.

1 12. (Previously Presented) The display apparatus of claim 11, wherein said video data  
2 converter converts said video data so as to correspond to the prescribed display mode when said  
3 synchronizing signal has a characteristic different from the prescribed display mode.

1 13. (Previously Presented) The display apparatus of claim 11, wherein said

2 synchronizing signal generator generates said synchronizing signal in correspondence to the  
3 prescribed display mode.

1 14. (Previously Presented) The display apparatus of claim 11, wherein said video data  
2 converter converts line and dot numbers of said video data so as to correspond to the prescribed  
3 display mode.

1 15. (Previously Presented) The display apparatus of claim 1, wherein said flat panel  
2 display apparatus operates without need for an analog-to-digital converter (ADC) or a phase-  
3 locked loop (PLL) circuit for signal conversion.

1 16. (Previously Presented) A digital data processing system comprising the  
2 combination of a host and a flat panel display apparatus as recited in claim 1, said system  
3 further comprising a transmitter connected to said host for transferring said display information  
4 as serial data from said host to said receiver of said flat panel display apparatus.

1 17. (Currently Amended) The system of claim 16, further comprising a video data  
2 converter connected between the output of said receiver and an input of said DAC for  
3 converting said video data so as to correspond to a prescribed display mode.

1 18. (Currently Amended) The system of claim 17, further comprising:  
2 a liquid crystal display (LCD) driver for receiving the converted video data ~~output~~ from

3 said video data converter and producing a driver output; and  
4 an LCD display panel for receiving [[an]] the driver output from said LCD driver.

1 19. (Previously Presented) The system of claim 17, wherein said video data converter  
2 converts said video data so as to correspond to the prescribed display mode when said  
3 synchronizing signal has a characteristic different from the prescribed display mode.

1 20. (Previously Presented) The system of claim 17, wherein said synchronizing signal  
2 generator generates said synchronizing signal in correspondence to the prescribed display mode.

1 21. (Previously Presented) The system of claim 17, wherein said video data converter  
2 converts line and dot numbers of said video data so as to correspond to the prescribed display  
3 mode.

1 22. (Previously Presented) The system of claim 16, wherein said flat panel display  
2 apparatus operates without need for an analog-to-digital converter (ADC) or a phase-locked  
3 loop (PLL) circuit for signal conversion.

1 23. (Currently Amended) The display apparatus of claim 2, said analog display  
2 apparatus comprising:

3 an amplifier for receiving said analog video signal from said DAC via said output  
4 terminal, and for amplifying said analog video signal;

5 a deflection signal generator for receiving said synchronizing signal ~~output~~ from said  
6 synchronizing signal generator via said output terminal, and for generating deflection signals;

7 a high voltage generator having an input connected to said deflection signal generator  
8 for receiving an output from said deflection signal generator, and for generating a high voltage;  
9 and

10 a cathode ray tube (CRT) display for receiving said amplified analog video signal from  
11 said amplifier, said deflection signals from said deflection signal generator, and ~~[[a]]~~ said high  
12 voltage from said high voltage generator.

1 24. (Currently Amended) The display apparatus of claim 2, further comprising:  
2 a liquid crystal display (LCD) driver for receiving the converted video data ~~output~~ from  
3 said video data converter and producing a driver output; and  
4 an LCD display panel for receiving ~~[[an]]~~ the driver output from said LCD driver.

1 25. (Previously Presented) The display apparatus of claim 2, wherein said video data  
2 converter converts said video data so as to correspond to the prescribed display mode when said  
3 synchronizing signal has a characteristic different from the prescribed display mode.

1 26. (Previously Presented) The display apparatus of claim 2, wherein said  
2 synchronizing signal generator generates said synchronizing signal in correspondence to the  
3 prescribed display mode.

1           27. (Previously Presented) A digital data processing system comprising the  
2 combination of a host and a flat panel display apparatus as recited in claim 2, said system  
3 further comprising a transmitter connected to said host for transferring said display information  
4 as serial data from said host to said receiver of said flat panel display apparatus.

1           28. (Currently Amended) The system of claim 27, further comprising:  
2 a liquid crystal display (LCD) driver for receiving the converted video data ~~output~~ from  
3 said video data converter and producing a driver output; and  
4 an LCD display panel for receiving [[an]] the driver output from said LCD driver.

1           29. (Previously Presented) The system of claim 27, wherein said video data converter  
2 converts said video data so as to correspond to the prescribed display mode when said  
3 synchronizing signal has a characteristic different from the prescribed display mode.

1           30. (Previously Presented) The system of claim 27, wherein said synchronizing signal  
2 generator generates said synchronizing signal in correspondence to the prescribed display mode.

1           31. (Previously Presented) The system of claim 27, wherein said video data converter  
2 converts line and dot numbers of said video data so as to correspond to the prescribed display  
3 mode.

1           32. (Previously Presented) The system of claim 27, wherein said flat panel display

2 apparatus operates without need for an analog-to-digital converter (ADC) or a phase-locked  
3 loop (PLL) circuit for signal conversion.

1 33. (Previously Presented) The display apparatus of claim 2, wherein said video data  
2 converter converts line and dot numbers of said video data so as to correspond to the prescribed  
3 display mode.

1 34. (Previously Presented) The display apparatus of claim 2, wherein said flat panel  
2 display apparatus operates without need for an analog-to-digital converter (ADC) or a phase-  
3 locked loop (PLL) circuit for signal conversion.

Claims 35 - 43. (Canceled)